

**CENTRAL LIBRARY**  
**MANIT, BHOPAL**

**BOOKS PROCURED IN FINANCIAL YEAR 2013-2014**

**Recommending Department : Electronics & Communication**

<b>S. No.</b>	<b>Author</b>	<b>Title</b>	<b>No. of Copies</b>
1	Weng Fook Lee	Verilog Coding For Logic Synthesis	1
2	Donald G Bailey	Design For Embedded Image Processing on FPGAs	1
3	S Palani	Automatic Control Systems	1
4	George Clayton	Operational Amplifiers	2