



# SMDP-C2SD

## Objective:

- To design an ASIC for Encryption and Decryption of serial data for transceiver.
- To design radiation hardened by design (RHBD) combinational cell library for 180 nm CMOS technology (SCL PDK).

## People:

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**Research Areas** : VLSI Signal Processing



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**Research Areas** : Semiconductor Device Simulation and  
Modelling



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**Research Areas** : Low power Digital design, Physical  
Design, Static Timing Analysis, Physical verification.

## **Resources:**

### **Software Tools**

- Cadence EDA Tools
- Synopsys EDA Tools
- Mentor EDA Tools
- Xilinx Vivado 16.2

### **Hardware**

- Basys 3 boards + Accessories
- Nexys4DDRs + Accessories
- Zybo Boards + Accessories
- Analog Discovery kit

### **Available Library**

- UMC bulk CMOS 180nm and 65nm
- SCL PDK 180nm

### **Systems**

- HP Z440 Servers – 5
- Client systems