

Advertisement for manpower required under the project : SMDP-C2SD

Advertisement No : MANIT/ECE/C2SD-SMDP/2018/2 dated 28.02.2018

Applications are invited in the prescribed format from eligible candidates for the following posts under SMDP-C2SD project (Under Ministry of Electronics and Information Technology). The positions are purely on contract basis and for project duration and based on the availability of funds under the provisions of the project.

S. No.	Posts	No. of Posts	Remuneration	Duration
1	Guest Faculty	01	Rs. 44,000/-pm (Consolidated)	1 Year*
2	Lab Engineer	01	Rs. 37,000/-pm (Consolidated)	1 Year*

*Tentative initial period (Extendable up to project duration only and subject to extant institute norms, satisfactory performance and availability of grants in the project).

Essential and Desirable Qualifications

1. For Guest Faculty

Essential Requirement:

- (i) BTech/BE in Electronics and Communication Engineering stream, I Division (minimum 65 % or 6.5 CGPA).
- (ii) Mtech/MS/ME in VLSI design/ Embedded system or in Electronics stream with specialization in VLSI design/Embedded systems, I Division (minimum 65 % or 6.5 CGPA).
- (iii) Knowledge of VLSI design tools and Linux.

Desirable:

- (i) PhD(awarded /submitted) in area relevant to VLSI/Embedded System stream.
- (ii) Teaching/Industry/Research experience.

2. For Lab Engineer:

Essential Requirement:

- (i) BTech/BE in Electronics and Communication Engineering stream / Computer Science Engineerig, I Division (minimum 65 % or 6.5 CGPA).
- (ii) Mtech/MS/ME in VLSI design/ Embedded system or in Electronics stream with specialization in VLSI design/Embedded systems, I Division (minimum 65 % or 6.5 CGPA).
- (iii) Knowledge of VLSI design tools and Linux.

Desirable:

- (i) Teaching/Industry/Research experience.

Other Instructions:

1. Applications (in prescribed format and supporting documents) to be received in the office of the Head of the Department of Electronics and Communication Engineering within 20 days from the date of publication of the advertisement on the institute web-site, to the following address.

Office of HOD,
Department of Electronics and Communication Engineering
Maulana Azad National Institute of technology
Bhopal-462003 (M.P.)

Application envelope to be marked with a line:

“Application for the post of(appropriate position to be mentioned here).....under SMDP-C2SD”

2. If an applicant wants him to be considered for more than one position, then separate applications will be required for each post, with indication of the candidates preference order for the positions.
3. After the last date of application, applications will be scrutinized for eligibility criteria as per the mentioned qualifications. In case of too many applications for a post shortlisting may be done for the next stage of interview at the institute.
4. Applicant is required to refer to the institute web-site (www.manit.ac.in) for the display of the list of shortlisted candidates for interview, and the date of interview. Interviews will be held at the institute within time-frame of 10-20 days after the display of list of eligible candidates for interview. Responsibility of checking the institute web-site will lie with the applicant.
5. No TA/DA will be reimbursable to the candidate shortlisted and called for the interview.

Approving authority:

DIRECTOR
MANIT Bhopal

APPLICATION FORMAT FOR PROJECT STAFF

(in SMDP-C2SD PROJECT of DeitY at MANIT Bhopal)

1. Post applied for:	Passport size photo to be pasted here
2. Full Name:	
3. Father's/Mother's Name:	
4. Address for communication	
Email:	
Phone/Mobile:	

5. Date of birth:

6. Educational Qualification-UG Degree onward(Attach copy of supporting certificates)

Sl. No.	Degree	Branch	VLSI/Embedded Systems related subject studied	Universities /Institute	% of marks/Grade point	Years of passing

7. Relevant Experience: (Attach copy of supporting certificates)

Sl. No.	Name of Employer	Designation	Nature of Duties	Period		
				From	To	Years and Months

8. Additional achievements/remarks, if any

(Attach separate sheet, if necessary)

DECLARATION

I hereby declare that I have read carefully and understood the above information and that all the entries in this application form are true to the best of my knowledge and belief. I will be responsible for disqualification at any stage if any information is found to be false

Place

Date:

Signature of candidate