



**AICTE Training And Learning (ATAL) Academy**  
**Short Term Training Program**  
**On**

**Advance VLSI Design and Embedded System**

**23<sup>rd</sup> – 27<sup>th</sup> Sept, 2019**

**Maulana Azad National Institute of Technology, Bhopal, MP 462003, India**

**ABOUT THE INSTITUTE:**

Maulana Azad National Institute of Technology, Bhopal is an Institute of national importance. It was formerly known as Maulana Azad College of Technology (MACT), which was India's one of the first eight Regional Engineering Colleges. The institute has been named after the great scholar, educationist and the first Education Minister of Govt. of India, Maulana Abul Kalam Azad. The Govt. of India and Govt. of Madhya Pradesh jointly sponsored the Institute in 1960 to attract bright young students from across the country. Presently, along with about 300 faculties and 6000 students, the Institute is successfully meeting the objective of producing skilled manpower of the highest quality to cope up with challenges of ever evolving industrial needs of the country. For more details, please visit at [www.manit.ac.in](http://www.manit.ac.in).

**COURSE CONTENTS:**

Advance VLSI Design and Embedded System training programme is a Combination of lecture and hands-on practice on VLSI and Embedded based System Design tools for providing innovative system design aspects by addressing the key developmental challenges of Wearable Devices, Internet of Things (IoT), and Automation System. The outcome of this program is to develop a comprehensive understanding of embedded system programming, design and its application. It will provide platform for discussion on recent trends in VLSI Design in the field of electronics.

**COURSE HIGHLIGHTS :**

- VLSI Design Flow and CAD Tools
- VHDL Synthesis and Verification
- IP Integrator and Embedded System Design Flow
- FPGA Based Design using Xilinx System Generator
- Embedded System Design for Wearable Devices
- Fundamentals of VLSI chip design methodology
- Basics of ASIC & FPGA Concepts of HDL design and verification for front end
- Synthesis technique overview w.r.t. ASIC & FPGA details of ASIC foundry & technology
- FPGA Hands on Session

**TARGET AUDIENCE:**

The course will be beneficial for Students, Faculty members and Industry professionals.

**RESOURCE PERSONS:**

Faculty Members and Industry Experts with expertise in VLSI design and development.

**HOW TO APPLY:**

There is no registration fee. The candidate may register online through following link latest by 20/9/2019:  
<https://docs.google.com/forms/d/1CwEsyqUncs9mav5l-053ovc0fzqObOJtyzV2boKjFhE/edit>

The seats are limited to 50 and selection will be on first come first serve basis.

Accommodation will be chargeable and no TA/DA will be provided.

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**Course coordinators:**

Dr Laxmi Kumre, Assistant Professor, ECE Department ([laxmikumre@manit.ac.in](mailto:laxmikumre@manit.ac.in)), 9406533670

Dr Bhavana P. Shrivastava, Assistant Professor, ECE Dept. ([sonibhavana1@gmail.com](mailto:sonibhavana1@gmail.com)), 9425635389

Dr Vijayshri Chaurasia, Assistant Professor, ECE Dept. ([vchaurasia@manit.ac.in](mailto:vchaurasia@manit.ac.in)), 9425011972